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Substitute for form 1449A/PTO <i>PTO & TRADEMA</i>		Application Number	10/723,948		
		Filing Date	11/26/2003		
		First Named Inventor:	Cornel D. Stanescu		
		Examiner: Y. J. Han	GROUP: 2838		
Sheet	1	of	1	Attorney Docket Number	CAT-12504-1C

U.S. PATENT DOCUMENTS

Examiner Initials*	Cited No. ¹	Document Number Number - Kind Code ² (<i>if known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Figures Appear
JP	C01	US-2004/0108844	A1	06/2004	Stanescu et al.
	C02	US-5,717,323		02/1998	Tailliet
	C03	US-6,201,491	B1	03/2001	Brunolli et al.
	C04	US-6,320,451	B1	11/2001	Harvey et al.
▼	C05	US-6,555,996	B2	04/2003	Drori et al.
		US-			
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		US-			

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Examiner Initials*	Cite No. ¹	Foreign Patent Document Cntry Code ³ Number ⁴ Kind Code ⁵ (<i>if known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Figures Appear	T ⁶

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER SIGNATURE: J. Han DATE CONSIDERED: 3/4/05

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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450. If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2. *EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

Substitute for form 1449A/PTO				Application Number	Filed Herewith 10723948
				Filing Date	November 26, 2003
				First Named Inventor:	STANESCU, Cornel D.
				Examiner name: Unknown	GROUP: Unknown 2838
Sheet	1	of	1	Attorney Docket Number	CAT-12504-1C

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JA	A01	4,271,486		Dagostino et al.	6/2/1981	
↓	A02	4,468,607		Tanaka et al.	8/28/1984	
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↓	A06	5,495,245		Ashe	2/27/1996	
↓	A07	6,201,491	B1	Brunolli et al.	3/13/2001	
↓	A08	6,331,768	B1	Drori et al.	12/18/2001	

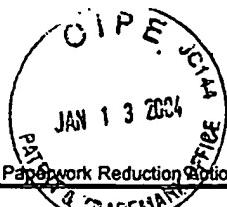
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

JA	Catalyst Semiconductor, Inc., "CAT515: 8-Bit Quad DACpot with RDY/BUSY and IND. Reference Inputs," 1997, pp.1-11. (No month)
↓	Catalyst Semiconductor, Inc., "CAT524: 8-Bit Quad Digital POT," 1998, pp. 25-36. (No month)
↓	Catalyst Semiconductor, Inc., "CAT514: 8-Bit Quad DACpot," 1997, pp. 1-13. (No month)
↓	Catalyst Semiconductor, Inc., "CAT514: 8-Bit Quad Digital POT," 1999, pp. 1-12. (No month)
↓	Catalyst Semiconductor, Inc., "CAT525: 8-Bit Quad Digital POT With Independent Reference Inputs," 1999, pp. 1-11. (No month)
↓	Catalyst Semiconductor, Inc., "CAT5111, CAT5112, CAT5113 and CAT5114: 100/32-Tap Digital POT with Independent Reference Inputs," 1999, pp. 1-6. (No month)
↓	Stanescu et al., "32 Taps Digitally Controlled Buffered Potentiometer," Proceedings of CAS 2001 International Semiconductor Conference, IEEE Catalog Number 01TH8547, pp. 535-538, October 2001.

EXAMINER J. Han DATE CONSIDERED 3/4/05

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 1 Attorney Docket Number CAT-12504-1C

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